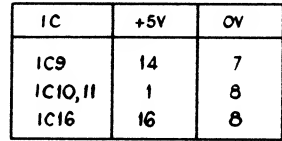


PRELIMINARY ISSUE
DATE 27 JUN 1983

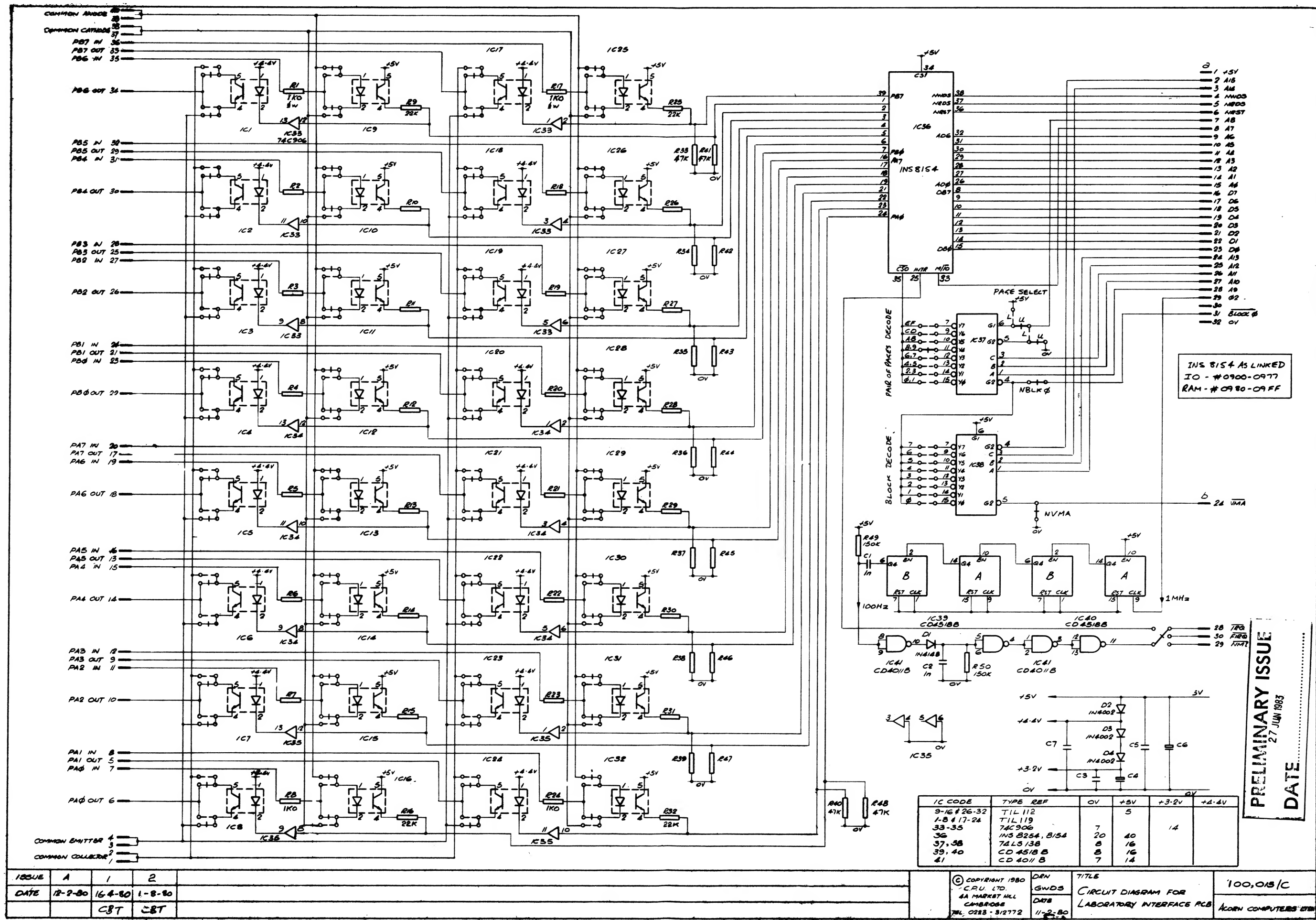


BOARD DECODED AT # 1A00-1A7F

PRELIMINARY ISSUE

DATE..... 27 JUN 1983

[illegible]



INS 8154 AS LINKED
IO - #0900-0977
RAM - #0980-09FF

PRELIMINARY ISSUE
27 JUN 1983
DATE

ISSUE	A	1	2
DATE	12-2-80	16-4-80	1-8-80
		CST	CST

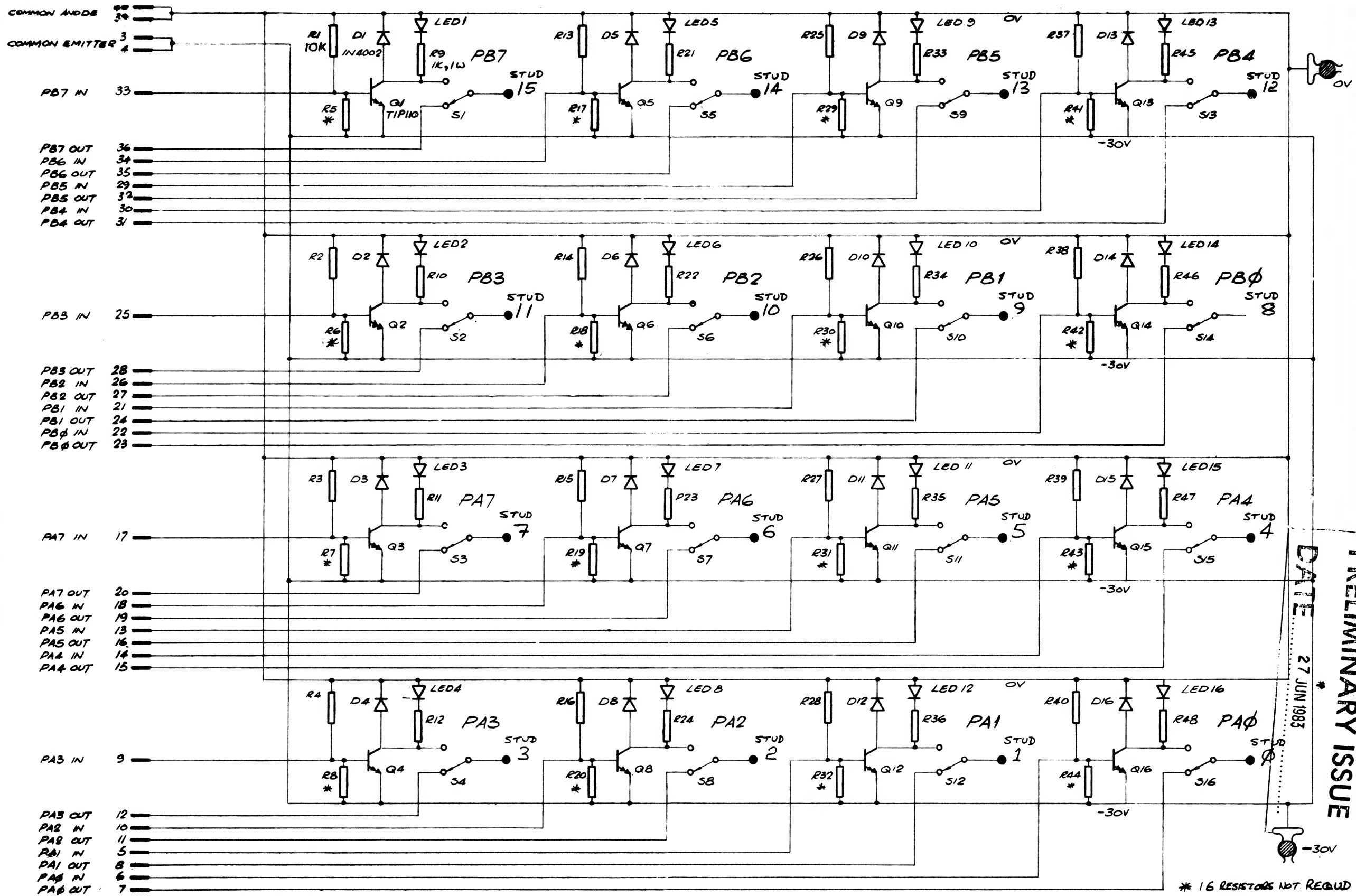
IC CODE	TYPE REF	OV	+5V	+3.2V	+4.4V
9-16 & 26-32	TIL 112				
1-8 & 17-24	TIL 119				
33-35	74C906				
36	INS 8254, B/54	20	40	14	
37, 38	74LS138	8	16		
39, 40	CD 4518 B	8	16		
41	CD 4011 B	7	14		

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CRU LTD.
4A MARKET HILL
CAMBRIDGE
TEL 0223 312772

DN
GWS
DATE
11-2-80

TITLE
CIRCUIT DIAGRAM FOR
LABORATORY INTERFACE PCB

100,018/C
ACORN COMPUTERS LTD

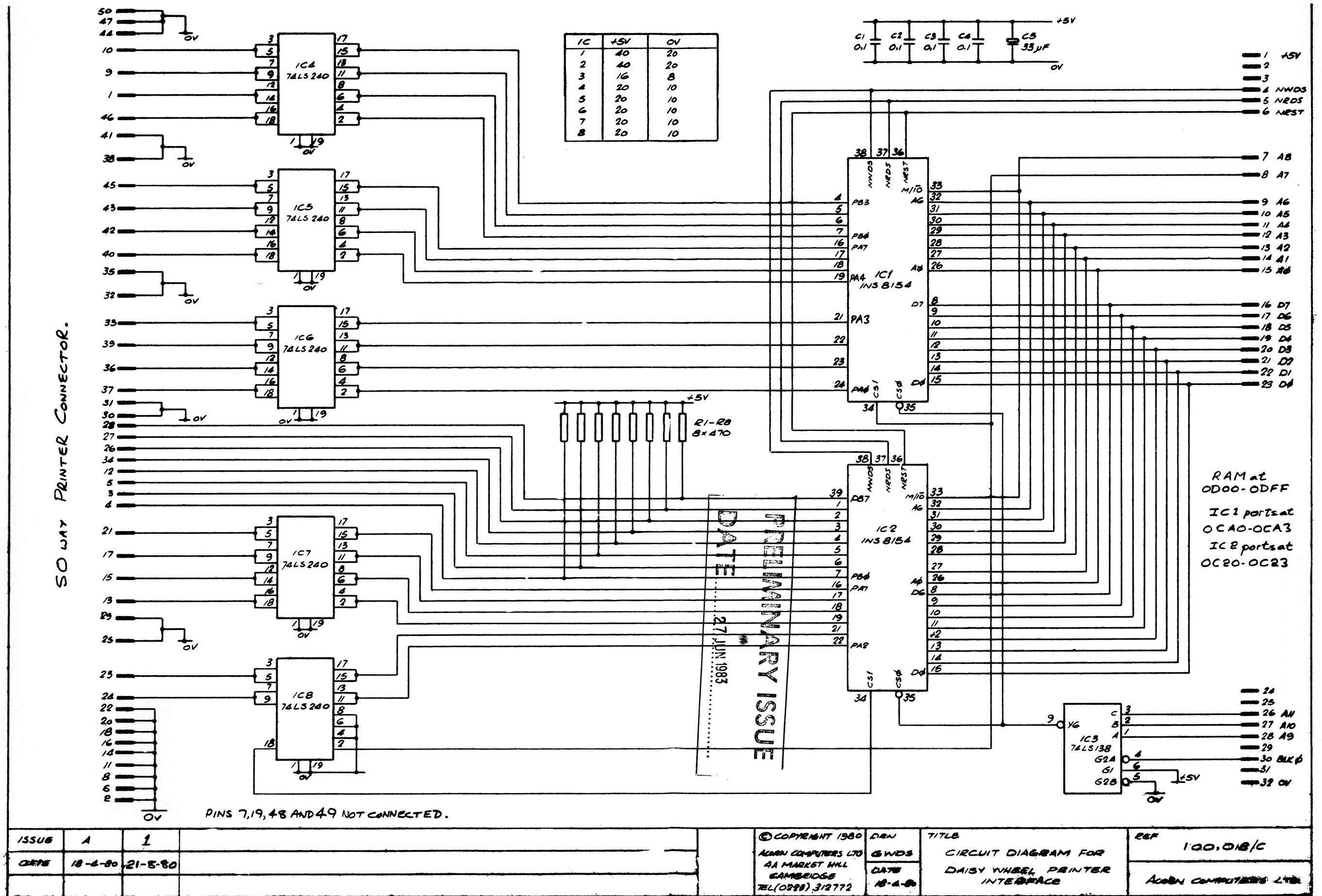


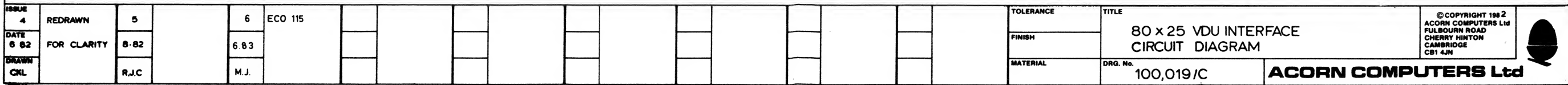
PRELIMINARY ISSUE

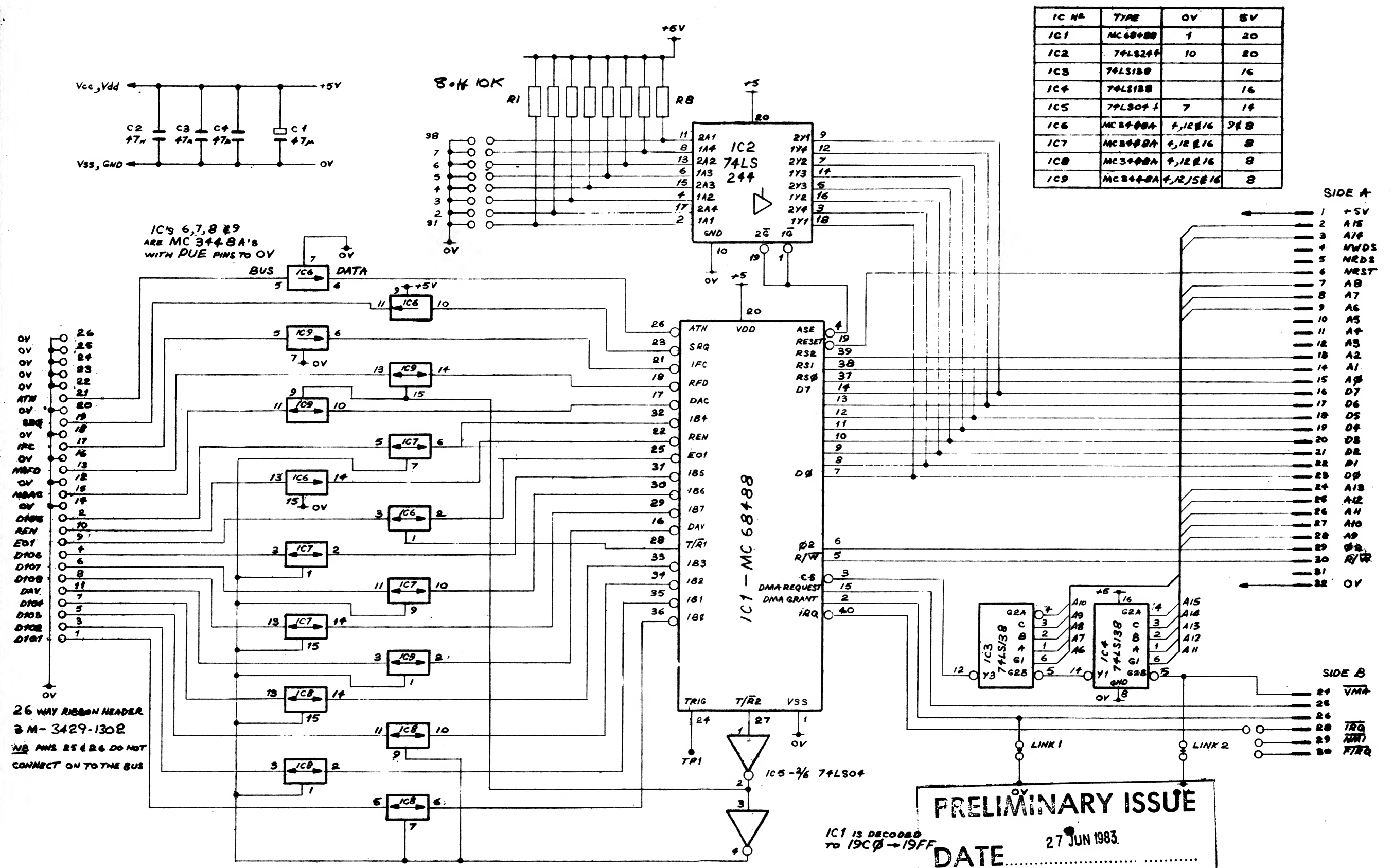
DATE 27 JUN 1983

* 16 RESISTORS NOT REQD

ISSUE	A	1	2		© COPYRIGHT 1980 ACORN COMPUTERS LTD. 4A MARKET HILL CAMBRIDGE TEL 0223 312772	DEN GWDS DATE 29-2-80	TITLE CIRCUIT DIAGRAM FOR INTERFACE PANEL PCB	100.017/C ACORN COMPUTERS LTD.
DATE	6-3-80	16-4-80	1-8-80					
		CBT	CRT					

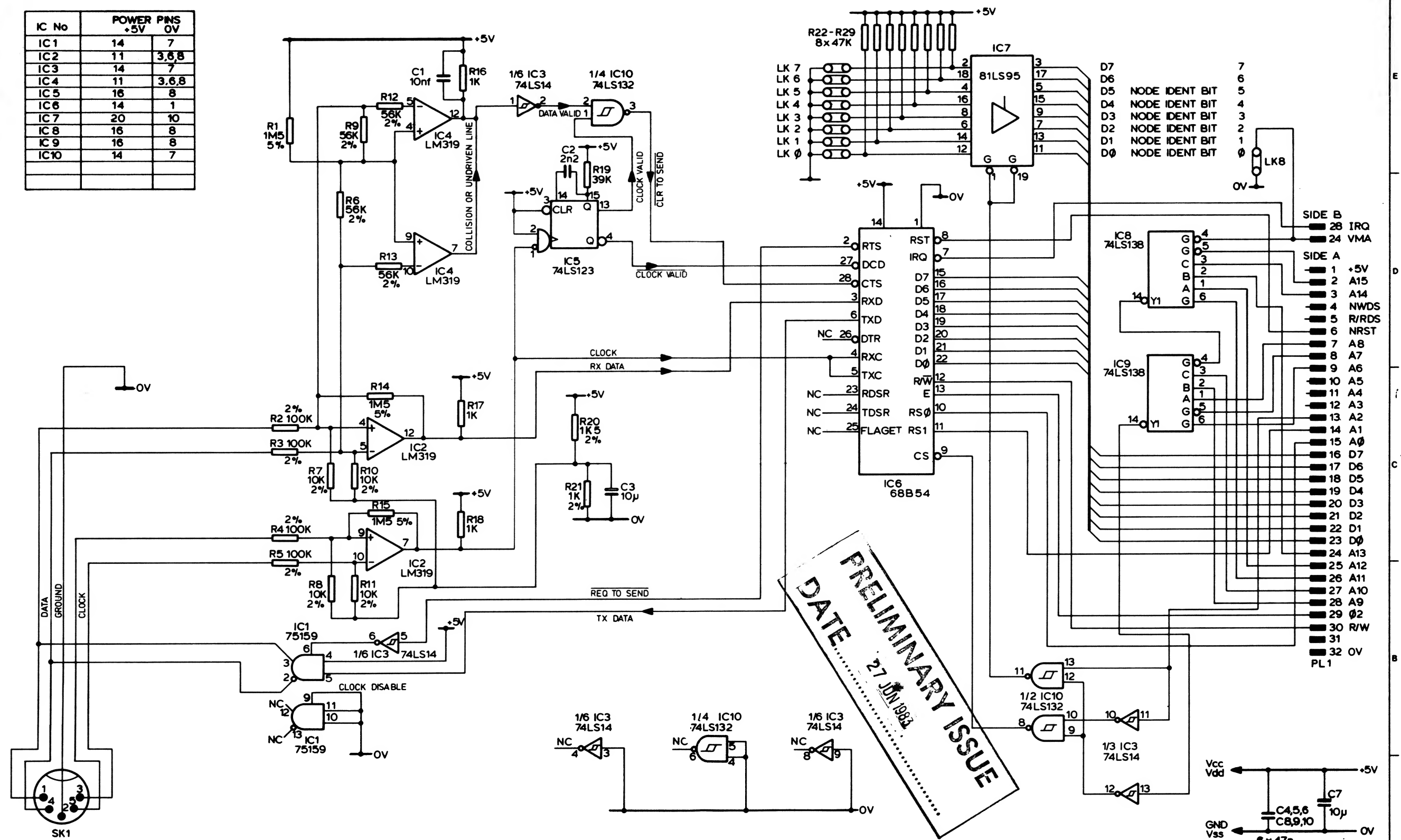


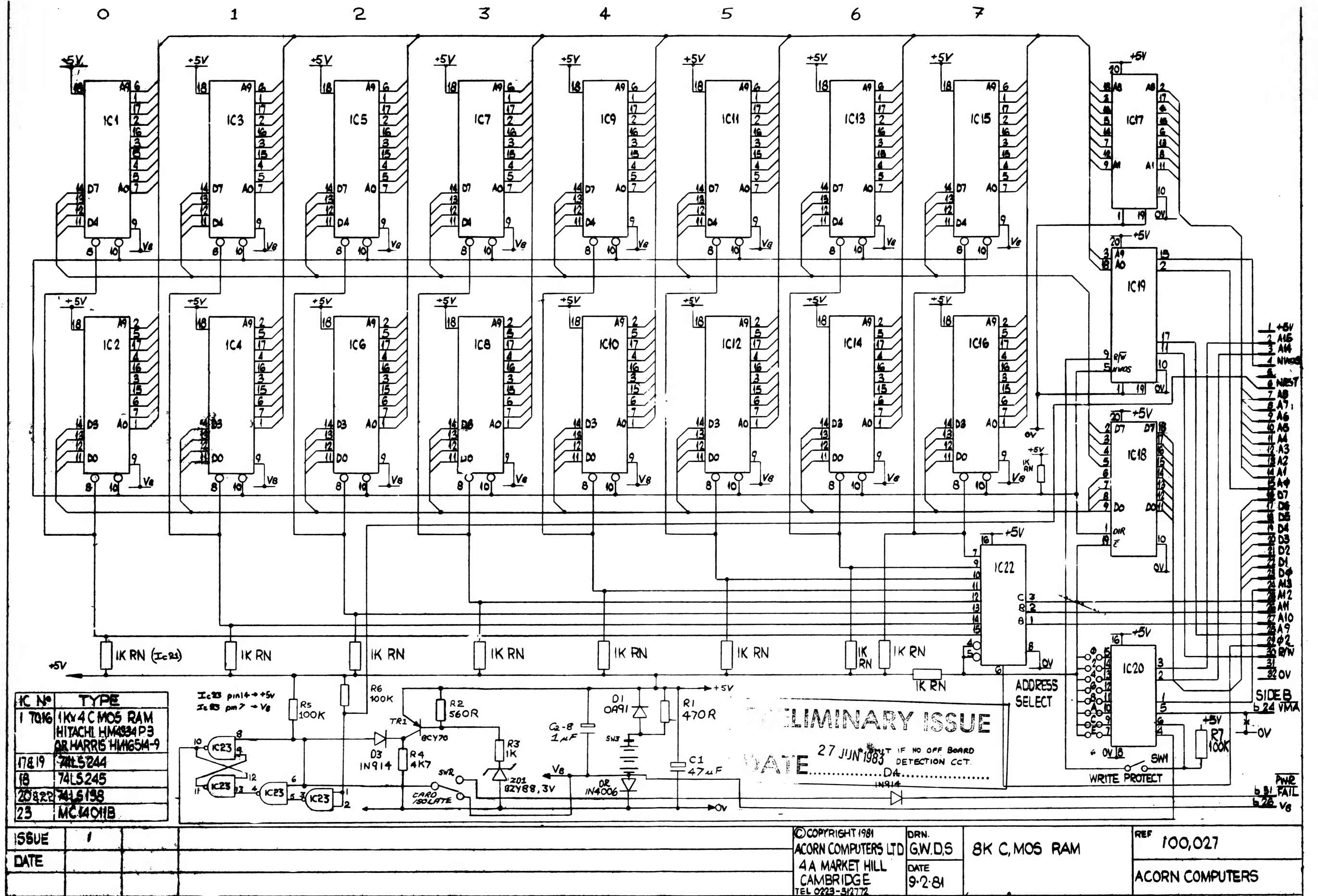


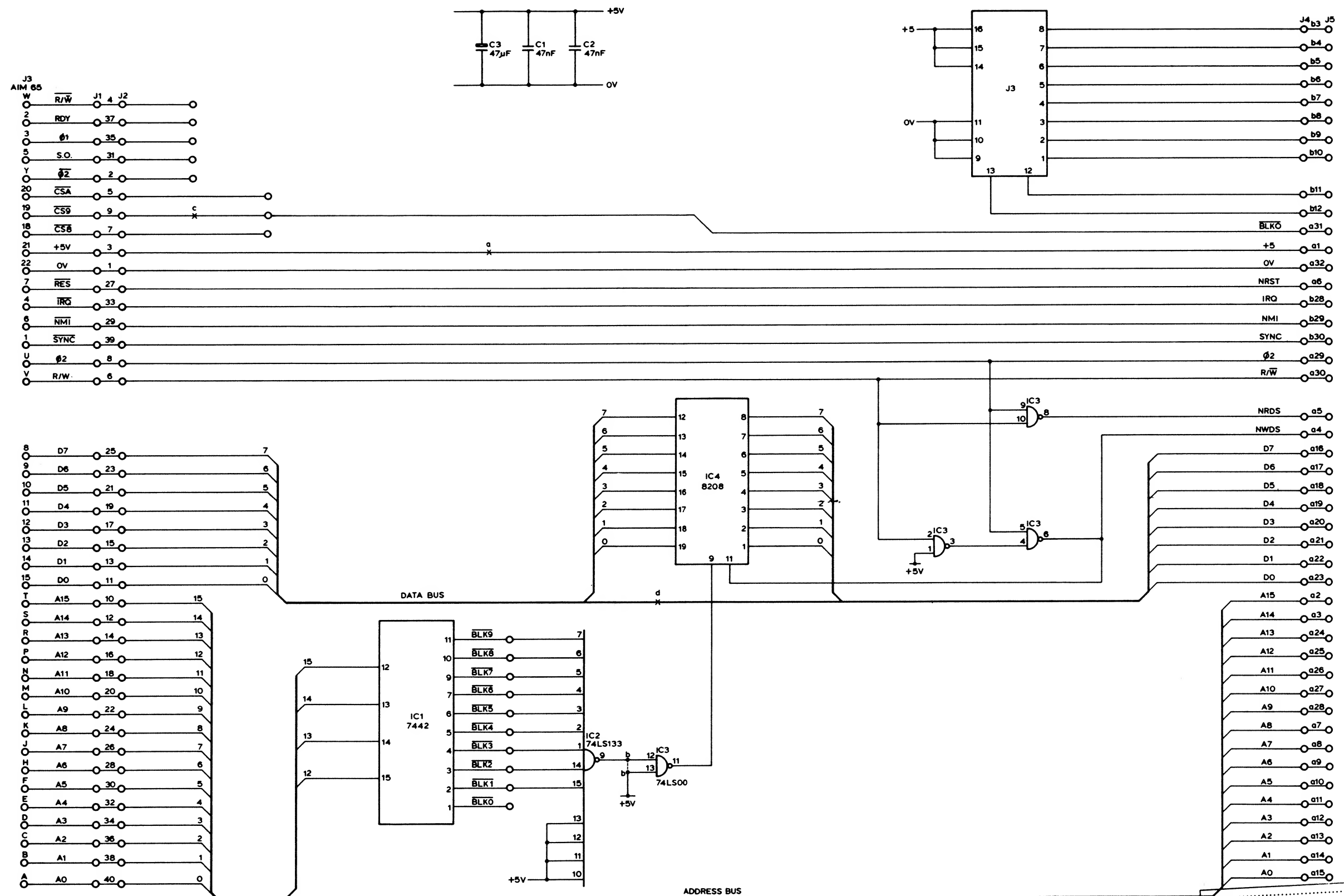


ISSUE	4	SCALE	2-1	© COPYRIGHT 1983 ACORN COMPUTERS 4A MARKET HILL CAMBRIDGE TEL: 0800-242776	DRN GWDS DATE 9-7-80	TITLE IEEE 488 BUS INTERFACE	REF 100,0214 ACORN COMPUTER
DATE	23-6-80						

IC No	POWER +5V	PINS OV
IC1	14	7
IC2	11	3,6,8
IC3	14	7
IC4	11	3,6,8
IC5	16	8
IC6	14	1
IC7	20	10
IC8	16	8
IC9	16	8
IC10	14	7

[illegible]





J4 DATA BUS FROM IC4/12-19
J5 DATA BUS FROM IC4/1- 8

PRELIMINARY ISSUE
27 JUN 1983
DATE

[illegible]